

Pb Free Plating Product

HER3020PT thru HER3060PT





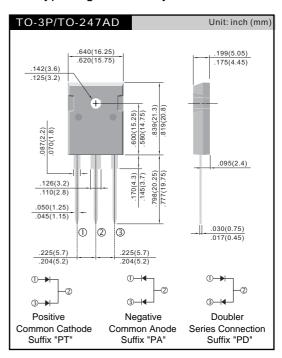
30.0 Ampere Dual SwitchMode Type High Efficiency Rectifier Diodes

Features

- ♦ Dual rectifier construction, positive center-tap
- Plastic package has Underwriters Laboratory Flammability Classification 94V0
- Glass passivated chip junctions
- Special for inverter/high power motor control
- ♦ Low forward voltage, high current capability
- ♦ Low thermal resistance
- ♦ Low power loss, high efficiency
- High temperature soldering guaranteed: 260°C, 0.16"(4.06mm)from case for 10 seconds

Mechanical Data

- Cases: TO-3P/TO-247AD molded plastic
- Terminals: Pure tin plated, lead free solderable per MIL-STD-750. Method 2026
- ♦ Polarity: As marked
- Mounting position: Any
- ♦ Mounting torque: 10in-lbs. Max.
- Weight: 0.2 ounce, 5.6 gram approximately



MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Rating at 25°C ambient temperature unless otherwise specified.

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitive load, derate current by 20%.

	SYMBOL	HER3020PT HER3020PA HER3020PD	HER3040PT HER3040PA HER3040PD	HER3060PT HER3060PA HER3060PD	UNIT
Maximum Recurrent Peak Reverse Voltage	VRRM	200	400	600	V
Maximum RMS Voltage	VRMS	140	280	420	V
Maximum DC Blocking Voltage	VDC	200	400	600	V
Maximum Average Forward Rectified Current Tc=125°C	IF(AV)	30.0			А
Peak Forward Surge Current, 8.3ms single Half sine-wave superimposed on rated load (JEDEC method)	IFSM	300			А
Maximum Instantaneous Forward Voltage @ 15.0 A	VF	0.98	1.3	1.7	V
Maximum DC Reverse Current @Tj=25°C At Rated DC Blocking Voltage @Tj=125°C	lR	10 500		uA uA	
Maximum Reverse Recovery Time (Note 1)	Trr	50		60	nS
Typical junction Capacitance (Note 2)	CJ	175 14		145	pF
Operating Junction and Storage Temperature Range	TJ, TSTG	-55 to +150			°C

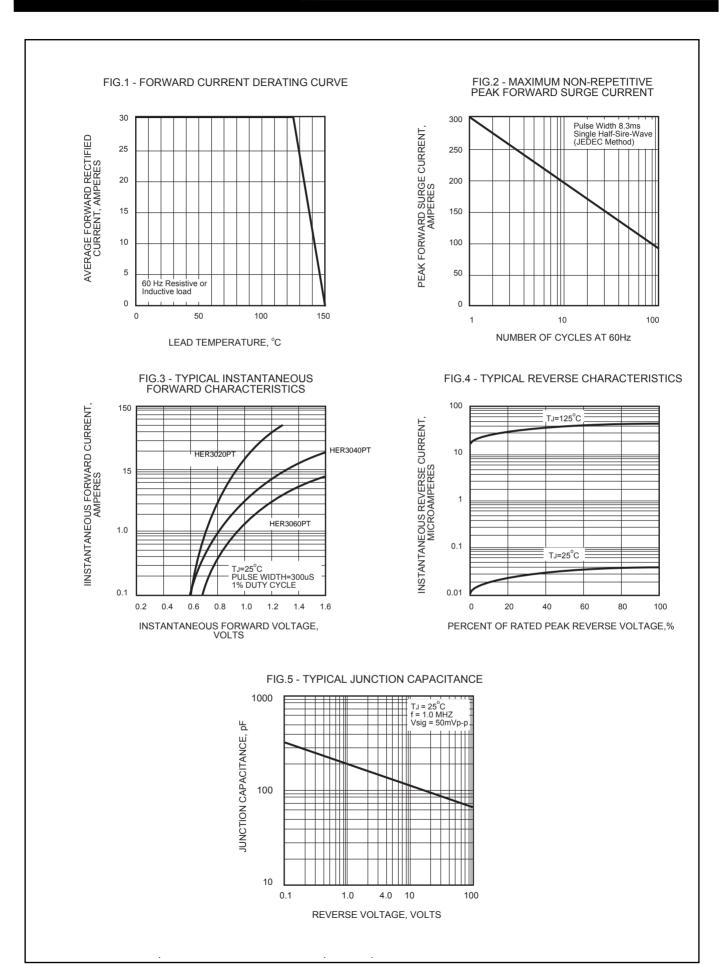
NOTES: (1) Reverse recovery test conditions IF = 0.5A IR = 1.0A Irr = 0.25A.

- (2) Thermal Resistance junction to terminal.
- (3) Measured at 1.0 MHz and applied reverse voltage of 4.0 Volts DC.

Rev.02

Page 1/2





Rev.02 Page 2/2