

Pb Free Plating Product

SFF2001G thru SFF2008G



20.0 Amperes Insulated Dual Common Cathode Super Fast Recovery Rectifiers

Features

- ★ Super fast switching for high efficiency
- ★ Low forward voltage drop
- ★ High current capability
- ★ Low reverse leakage current
- ★ High surge current capability

Application

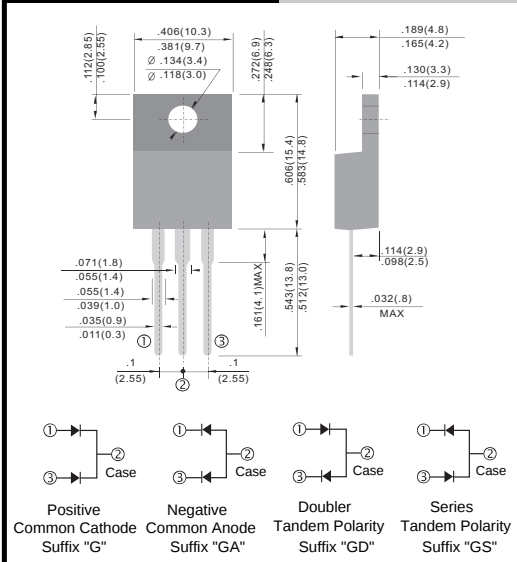
- ★ Automotive Inverters and Solar Inverters
- ★ Plating Power Supply, SMPS and UPS
- ★ Car Audio Amplifiers and Sound Device Systems

Mechanical Data

- ★ Case: ITO-220AB full plastic isolated package
- ★ Epoxy: UL 94V-0 rate flame retardant
- ★ Terminals: Solderable per MIL-STD-202 method 208
- ★ Polarity: As marked on diode body
- ★ Mounting position: Any
- ★ Weight: 1.90 gram approximately

ITO-220AB/TO-220F-3L

Unit: inch (mm)

**MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS (T_A=25°C unless otherwise noted)**

PARAMETER	SYMBOL	SFF 2001 G	SFF 2002 G	SFF 2003 G	SFF 2004 G	SFF 2005 G	SFF 2006 G	SFF 2007 G	SFF 2008 G	UNIT
Maximum repetitive peak reverse voltage	V _{RRM}	50	100	150	200	300	400	500	600	V
Maximum RMS voltage	V _{RMS}	35	70	105	140	210	280	350	420	V
Maximum DC blocking voltage	V _{DC}	50	100	150	200	300	400	500	600	V
Maximum average forward rectified current	I _{F(AV)}	20								A
Peak forward surge current, 8.3 ms single half sine-wave superimposed on rated load	I _{FSM}	150								A
Maximum instantaneous forward voltage (Note 1) @ 10 A	V _F	0.975				1.3		1.7		V
Maximum reverse current @ rated V _R T _J =25°C T _J =125°C	I _R	10 400								μA
Maximum reverse recovery time (Note 2)	t _{rr}	35								ns
Typical junction capacitance (Note 3)	C _J	90								pF
Typical thermal resistance	R _{θJC}	2.5								°C/W
Operating junction temperature range	T _J	- 55 to +150								°C
Storage temperature range	T _{STG}	- 55 to +150								°C

Note 1: Pulse test with PW=300μs, 1% duty cycle

Note 2: Test conditions: I_F=0.5A, I_R=1.0A, I_{RR}=0.25A.

Note 3: Measured at 1 MHz and applied reverse voltage of 4.0 V DC.

RATINGS AND CHARACTERISTICS CURVES

($T_A=25^{\circ}\text{C}$ unless otherwise noted)

FIG. 1 FORWARD CURRENT DERATING CURVE

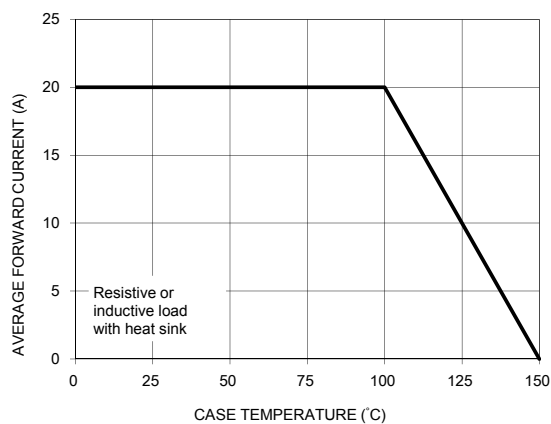


FIG. 2 TYPICAL REVERSE CHARACTERISTICS

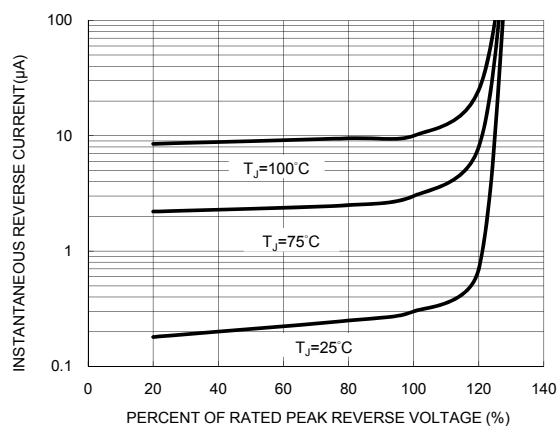


FIG. 3 MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

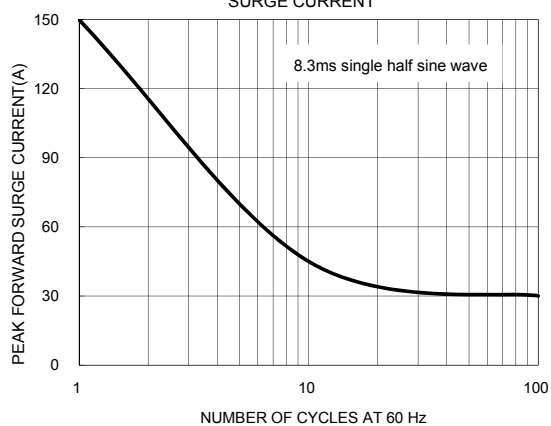


FIG. 4 TYPICAL FORWARD CHARACTERISTICS

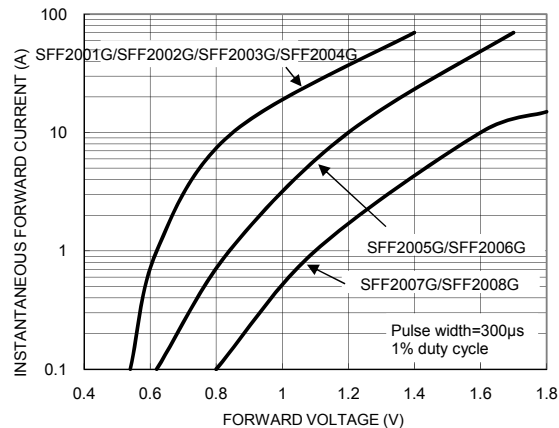


FIG. 5 TYPICAL JUNCTION CAPACITANCE

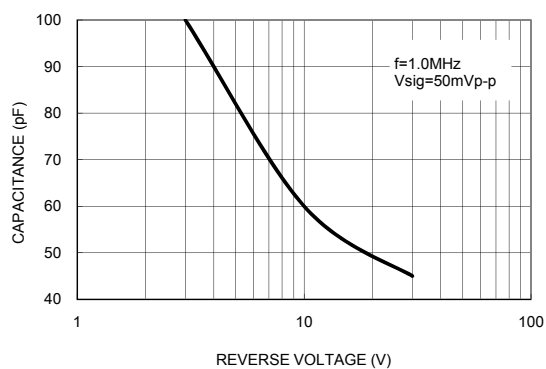


FIG. 6 REVERSE RECOVERY TIME CHARACTERISTIC AND TEST CIRCUIT DIAGRAM

